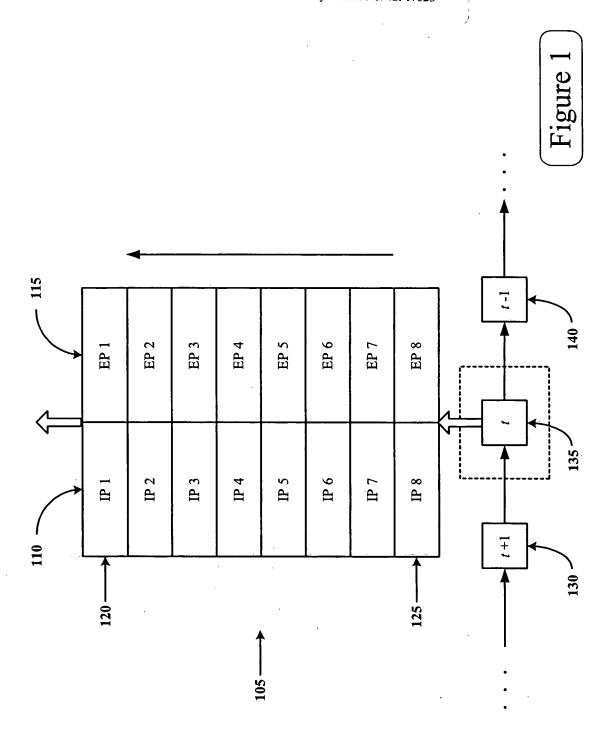
Title: Memory Trace Buffer
Inventors: Mauricio J. Serrano, et al.
Filing Date: Concurrently Herewith
Express Mail Label No.: EV 331619477 US

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BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Title: Memory Trace Buffer
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Attorney Docket No: 42P17023

220	Instruction Address IP1 IP2 IP3 IP4 IP5 IP6	215 Effective Address  0x  0x  0x  0x  0x  0x	235 [Effective Address] 0xBEB0	240 Base Address OxBCB0	Instruction  MOV EDX, [EAX+8]  MOV EBX, [EDX+12]
ŕ	IP7				
230	IP8	0xBABA	0xABAB	0xBAB0	MOV ECX, [EBX+EDX*4]

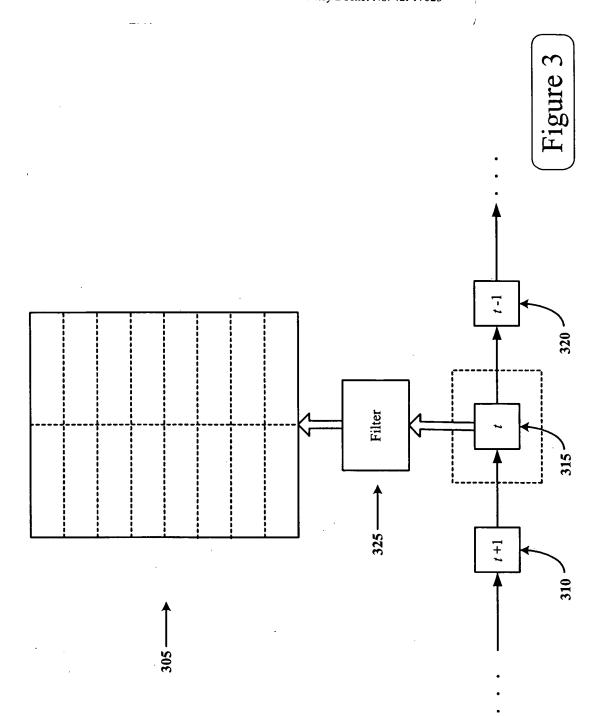
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Title: Memory Trace Buffer (303) 740-1980

Inventors: Mauricio J. Serrano, et al.

Filing Date: Concurrently Herewith Express Mail Label No.: EV 331619477 US

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Title: Memory Trace Buffer Inventors: Mauricio J. Serrano, et al. Filing Date: Concurrently Herewith Express Mail Label No.: EV 331619477 US

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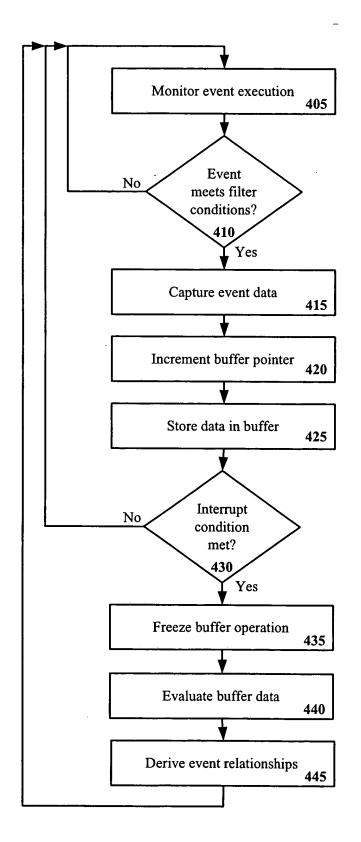


Figure 4

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Title: Memory Trace Buffer (303) 740-1980

Inventors: Mauricio J. Serrano, et al. Filing Date: Concurrently Herewith Express Mail Label No.: EV 331619477 US

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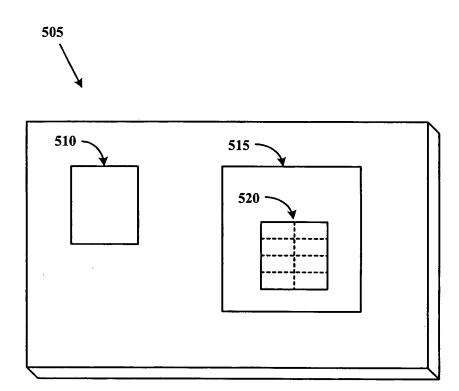


Figure 5

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Title: Memory Trace Buffer
Inventors: Mauricio J. Serrano, et al.
Filing Date: Concurrently Herewith
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Sheet: 6 of 6

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